

FORM PTO-1390 (Modified)
(REV 10-95)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)
CONCERNING A FILING UNDER 35 U.S.C. 371

9319S-000063

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 1.5)

097068052

INTERNATIONAL APPLICATION NO.

PCT/JP97/03018

INTERNATIONAL FILING DATE

August 28, 1997

PRIORITY DATE CLAIMED

August 29, 1996

TITLE OF INVENTION

Liquid Crystal Display Device and Method of Manufacturing the Same

APPLICANT(S) FOR DO/EO/US

Kiyotaka KOIDE

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).
4. ☐ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.
5. ☒ A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
 - a. ☐ is transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☒ has been transmitted by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ A translation of the International Application into English (35 U.S.C. 371(c)(2)).
7. ☒ A copy of the International Search Report (PCT/ISA/210).
8. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
 - a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ have been transmitted by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☒ have not been made and will not be made.
9. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
10. ☒ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).
11. ☐ A copy of the International Preliminary Examination Report (PCT/PEA/409).
12. ☐ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).

Items 13 to 18 below concern document(s) or information included:

13. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☒ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☒ A **FIRST** preliminary amendment.
A **SECOND** or **SUBSEQUENT** preliminary amendment.
16. ☐ A substitute specification.
17. ☐ A change of power of attorney and/or address letter.
18. ☒ Certificate of Mailing by Express Mail
19. ☒ Other items or information:
Translation of International Search Report; Form PTO-1449 with copies of the references cited therein (3 Japan); and 8 sheets of drawings showing FIGS. 1-10.

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 1.5)	INTERNATIONAL APPLICATION NO.	ATTORNEY'S DOCKET NUMBER
N/A	PCT/JP97/03018	9319S-000063

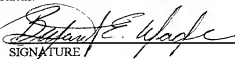
20. The following fees are submitted:				CALCULATIONS PTO USE ONLY	
BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :					
<input checked="" type="checkbox"/>	Search Report has been prepared by the EPO or JPO	\$930.00			
<input type="checkbox"/>	International preliminary examination fee paid to USPTO (37 CFR 1.482)	\$720.00			
<input type="checkbox"/>	No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2))	\$790.00			
<input type="checkbox"/>	Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO	\$1,070.00			
<input type="checkbox"/>	International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4)	\$98.00			
ENTER APPROPRIATE BASIC FEE AMOUNT =				\$930.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than months from the earliest claimed priority date (37 CFR 1.492 (e)). <input checked="" type="checkbox"/> 20 <input type="checkbox"/> 30				\$130.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	17 - 20 =	0	x \$22.00	\$0.00	
Independent claims	4 - 3 =	1	x \$82.00	\$82.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>				\$0.00	
TOTAL OF ABOVE CALCULATIONS =				\$1,142.00	
Reduction of 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed (Note 37 CFR 1.9, 1.27, 1.28) (check if applicable) <input type="checkbox"/>				\$0.00	
SUBTOTAL =				\$1,142.00	
Processing fee of \$130.00 for furnishing the English translation later than months from the earliest claimed priority date (37 CFR 1.492 (f)). <input type="checkbox"/> 20 <input type="checkbox"/> 30 +				\$0.00	
TOTAL NATIONAL FEE =				\$1,142.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable) <input checked="" type="checkbox"/>				\$40.00	
TOTAL FEES ENCLOSED =				\$1,182.00	
				Amount to be refunded	\$
				charged	\$

- ☒ A check in the amount of \$1,182.00 to cover the above fees is enclosed.
- ☐ Please charge my Deposit Account No. _____ in the amount of _____ to cover the above fees.
A duplicate copy of this sheet is enclosed.
- ☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 08-0750 A duplicate copy of this sheet is enclosed.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

G. Gregory Schivley, Esq.
Harness, Dickey & Pierce, P.L.C.
P.O. Box 828
Bloomfield Hills, MI 48303


SIGNATURE

Bryant E. Wade

NAME

40,344

REGISTRATION NUMBER

April 29, 1998
DATE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kiyotaka Koide)
)
International)
Application No. PCT/JP97/03018)
)
International)
Filing Date: August 28, 1997) **PRELIMINARY AMENDMENT**
)
For: Liquid Crystal Display Device and Method)
of Manufacturing the Same)
_____)

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as express mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on April 29, 1998.

By 

Sir:

Prior to the examination of this application, please amend it as follows:

IN THE SPECIFICATION

Page 2, line 3, please delete "device being" and substitute --devices-- therefor.

Page 2, line 4, please delete "has" and substitute --have-- therefor.

Page 2, line 9, please delete "is" and substitute --are-- therefor.

Page 3, line 2, please delete "a".

Page 3, line 4, after "However," , please insert --if--.

Page 3, line 16, after "may", please insert --be--.

Page 5, line 3, please delete "an" and substitute --A-- therefor.

Page 6, line 17, please delete "an" and substitute --a-- therefor.

Page 9, line 4, please delete "an" and substitute --a-- therefor.

Page 10, line 13, please delete "orientation" and substitute --alignment-- therefor.

Page 10, line 15, please delete "paining" and substitute --applying-- therefor.

Page 12, line 2, please delete "determined" and substitute --determine-- therefor.

Page 16, line 3, please delete "an A - A line" and substitute --line A-A-- therefor.

Page 16, line 23, please delete "the A - A line" and substitute --line A-A-- therefor.

IN THE CLAIMS

Please amend the claims as follows:

Before Claim 1, please insert "What is claimed is:"

Claim 1, lines 4-5, please delete "directly or indirectly connected" and substitute --coupled-- therefor.

Claim 3, line 2, please delete "an" and substitute --a-- therefor.

Claim 6, line 5, please delete "directly or indirectly connected" and substitute --coupled-- therefor.

Claim 7, line 2, please delete "claim 5," and substitute "Claim 6," therefor.

Claim 7, line 2, please delete "an" and substitute --a-- therefor.

Please add the following new claims:

8. (NEW) A liquid-crystal display device comprising:

a first substrate having an inner surface;
a second substrate having an inner surface;
a liquid-crystal layer disposed between said first and second substrates;
a wiring layer formed on at least one of said inner surfaces of said first and second substrates, said wiring layer including a connection portion;
a pixel electrode coupled to said connection portion of said wiring layer; and
an insulating film formed on a surface of said wiring layer.

9. (NEW) The liquid-crystal display device of Claim 8 wherein said insulating film is insulatingly disposed between said wiring layer and said pixel electrode.

10. (NEW) The liquid-crystal display device of Claim 9 wherein said insulating film includes an opening through which said pixel electrode is coupled to said wiring layer.

11. (NEW) The liquid-crystal display device of Claim 9 further comprising a MIM element coupled between said connection portion and said pixel electrode.

12. (NEW) The liquid-crystal display device of Claim 11 wherein said insulating film covers a surface of said MIM element.

13. (NEW) The liquid-crystal display device of Claim 8 further comprising a TFT element coupled between said connection portion and said pixel electrode.

14. (NEW) The liquid-crystal display device of Claim 13 wherein said insulating film is formed on said wiring layer connected to said TFT element.

15. (NEW) The liquid-crystal display device of Claim 8 wherein said insulating film has light shading properties.

16. (NEW) A method of forming a liquid-crystal display device comprising:
providing a first substrate having an inner surface;
providing a second substrate having an inner surface;
forming a wiring layer on at least one of said inner surfaces of said first and second substrates;
forming an insulating film over a surface of said wiring layer;
forming a pixel electrode such that a peripheral portion thereof is arranged on said insulating film;
coupling said pixel electrode to a connection portion of said wiring layer; and
interposing a liquid crystal layer between said first and second substrates.

17. (NEW) The method of Claim 16 further comprising:
connecting a MIM element between said connection portion and said pixel electrode and forming said insulating film so as to cover a surface of said MIM element.

IN THE ABSTRACT

Page 23, line 14, please delete "(to be described later)".

REMARKS

The purpose of this preliminary amendment is to clarify the application as originally filed and to add additional claims to provide applicants with a scope of protection commensurate with their contribution to the art.

Favorable consideration of this application is respectfully requested.

Respectfully submitted,

Date: April 29, 1998

By: 

G. Gregory Schivley
Reg. No. 27,382
Bryant E. Wade
Reg. No. 40,344
Attorneys for Applicants

Harness, Dickey & Pierce, P.L.C.
P.O. Box 828
Bloomfield Hills, MI 48303
(248) 641-1600

GGG/BEW/cmb

- 1 -

DESCRIPTION

LIQUID-CRYSTAL DISPLAY DEVICE AND
METHOD OF MANUFACTURING THE SAME

Technical Field

The present invention relates to a liquid-crystal display device and a method of manufacturing the same and, more particularly, to an inner surface structure of a substrate constituting a liquid-crystal panel.

Background Art

A liquid-crystal display device generally comprises a liquid-crystal panel in which two substrates having inner surfaces on which electrodes corresponding to a large number of pixel regions arranged on a display surface are arranged are formed to hold a liquid-crystal layer between the substrates. In order to drive the liquid-crystal display device, an electric field is applied to the liquid-crystal layer by the electrodes to change the optical characteristics of the liquid-crystal layer, so that various displays can be performed. In this case, wiring layers for applying a predetermined drive potential to the electrodes may be arranged in parallel.

In an active matrix liquid-crystal display device, active elements such as TFT (Thin Film Transistors) elements or MIM (Metal-Insulator-Metal) elements are connected to the wiring layers formed on the inner surfaces of element substrates.

These active elements are connected to pixel electrodes formed in pixel regions, respectively.

Some liquid-crystal display device being capable of performing a color display has the following structure. That is, color resists or the like each having a predetermined pattern are formed on the inner surface of the opposite substrate opposing an element substrate on which the active elements are formed, so that a color filter in which coloring layers, i.e., red (R), green (G), and blue (B) layers is formed. The color filter is covered with an overcoat film, and an opposite transparent electrode consisting of an ITO (Indium-Tin Oxide) is formed.

In the above conventional liquid-crystal display device, a liquid-crystal layer is sealed between two substrates, and a predetermined voltage is applied across two opposite electrodes formed on the inner surfaces of both the substrates to change the orientation of liquid-crystal molecules. However, in steps in manufacturing the liquid-crystal display device, conductive foreign matter may be inserted between the two substrates or into the liquid crystal. In this case, the conductive foreign matter may be interposed between the electrodes formed on the two substrates to adversely affect an electric field to be applied to the liquid crystal, or a point defect may be generated by the short circuit between the electrodes.

Here, even if the conductive foreign matter is inserted

into a pixel region to short-circuit the electrodes, only the corresponding a pixel is not operated in the active matrix liquid-crystal display device, i.e., only a point defect is generated. However, similar foreign matter is located above a wiring layer or an active element on an element substrate, the wiring layer and the opposite electrode may be short-circuited. In this case, a crossline-like line defect may be generated on the display surface. Unlike the above point defect, generation of such a line defect means that a liquid-crystal panel is defective. Therefore, the line defect considerably degrades the manufacturing yield of the liquid-crystal display device to seriously affect manufacturing steps or manufacturing cost.

In particular, even if abnormality is not detected in the manufacturing steps, such a defect caused by conductive foreign matter may retroactively generated by the foreign matter moving in the panel after shipping. For this reason, the defect cannot be easily prevented by inspections in the manufacturing steps.

Therefore, perfect countermeasures against the defect caused by foreign matter are necessary.

Therefore, the present invention has been made to solve the above problem, and has as its object to prevent a display defect caused by a short circuit between upper and lower electrodes by using a structure in which two substrates in a liquid-crystal display device are not easily short-circuited.

Disclosure of Invention

In order to solve the above problem, a means provided by the present invention is a liquid-crystal display device comprising two substrates which interpose a liquid-crystal layer therebetween, a wiring layer formed on at least one inner surface of the substrates, and a pixel electrode directly or indirectly connected to a connection portion of the wiring layer, characterized in that an insulating film is formed on a surface of the wiring layer.

According to this means, when the surface of the wiring layer is covered with the insulating layer, contact of conductive foreign matter to the wiring layer and a short circuit between the wiring layer and the opposite electrode through the foreign matter can be prevented, and a severe line defect can be prevented from being generated.

Here, the insulating film is preferably arranged to insulate the wiring layer and the pixel electrode from each other, and preferably has an opening for assuring the electric conductivity between the connection portion and the pixel electrode.

According to this means, since the insulating film is formed not only to insulate the surface of the wiring layer, but also to insulate the wiring layer and the pixel electrode from each other, the wiring layer and the pixel electrode are not in contact with each other except for the electric conductive portion between the connection portion and the pixel electrode. For this reason, degradation of a display state

caused by the short circuit between the wiring layer and the pixel electrode can be prevented.

An MIM element is formed between the connection portion and the pixel electrode, and the insulating film preferably also covers the surface of the MIM element.

According to this means, since the insulating film also covers the surface of the MIM element, contact between the MIM element and conductive foreign matter and a short circuit between the MIM element and the opposite electrode through the foreign matter can be prevented.

A TFT element is formed between the connection portion and the pixel electrode, and the insulating film is preferably formed on the wiring layer connected to the TFT element.

According to this means, contact between the wiring layer connected to the TFT element and conductive foreign matter and a short circuit between the wiring layer and the opposite electrode through the foreign matter can be prevented.

The insulating film preferably has light-shielding properties.

According to this means, since a light-shielding member need not be arranged on a substrate on which a color filter is formed, manufacturing cost can be reduced, and the opposite electrodes need not be arranged with a high alignment precision.

In a method of manufacturing a liquid-crystal display device comprising two substrates which interpose a liquid-crystal layer therebetween, a wiring layer formed on

at least one inner surface of the substrates, and a pixel electrode directly or indirectly connected to a connection portion of the wiring layer,

after the wiring layer is formed on the inner surface of the substrates, an insulating film is formed to cover the surface of the wiring layer and a portion between the wiring layer and the pixel electrode, and then, the pixel electrode is formed such that the peripheral portion of the pixel electrode is arranged on the insulating film.

According to this means, the surface of the wiring layer can be covered with the insulating film, and the wiring layer and the pixel electrode can be insulated from each other by the insulating film. For this reason, the insulating properties between the opposite substrates and the insulating properties between the wiring layer and the pixel electrode can be assured.

In this case, after an MIM element connected between the connection portion and the pixel electrode is formed, the insulating film is preferably formed to also cover the surface of the MIM element.

Brief Description of Drawings

FIG. 1 is a schematic enlarged plan view showing a planar structure on an element substrate in an embodiment of a liquid-crystal display device according to the present invention.

FIG. 2 is an enlarged longitudinal sectional view showing

the sectional structure of this embodiment.

FIG. 3 is a schematic process drawing showing an embodiment of a method of manufacturing a liquid-crystal display device according to the present invention.

FIG. 4 is a schematic process drawing showing the embodiment of the method of manufacturing a liquid-crystal display device according to the present invention.

FIG. 5 is a schematic process drawing showing the embodiment of the method of manufacturing a liquid-crystal display device according to the present invention.

FIG. 6 is a schematic process drawing showing the embodiment of the method of manufacturing a liquid-crystal display device according to the present invention.

FIG. 7 is a schematic view showing the structure of a liquid-crystal display device according to the present invention.

FIG. 8 is a schematic enlarged plan view showing a planar structure on an element substrate in an embodiment of a liquid-crystal display device according to the present invention.

FIG. 9 is an enlarged longitudinal sectional view showing the sectional structure of this embodiment.

FIG. 10 is a schematic view showing the structure of a liquid-crystal display device according to the present invention.

Best Mode for Carrying Out the Invention

Embodiments according to the present invention will be described below with reference to the accompanying drawings.
(First Embodiment)

FIGS. 1 and 2 show an embodiment of a liquid-crystal display device according to the present invention. This embodiment exemplifies a case wherein the present invention is applied to an active matrix liquid-crystal display device having MIM elements respectively formed in pixel regions.

In this embodiment, as shown in FIG. 2, a transparent underlying layer 11 is formed on the surface of an element substrate 10. The underlying layer 11 is used to improve the adhesion between the element substrate 10 and a film such as a wiring layer formed on the element substrate 10. A wiring layer 12 constituted by a metal thin film is formed on the surface of the underlying layer 11 to have a predetermined pattern as shown in FIG. 1. A first electrode portion 13 formed to project to each pixel region is integrally formed with the wiring layer 12.

A thin insulating layer 14 formed by an anodic oxidation method is formed on the surfaces of the wiring layer and the first electrode portion 13, and a second electrode layer 15 is formed through the thin insulating layer 14. The second electrode layer 15 comprises an electrode portion 15a located immediately above the first electrode portion 13 and a pixel contact portion 15b which extends from the electrode portion

15a to largely overhang.

The first electrode portion 13, the thin insulating layer 14, and the electrode portion 15a of the second electrode layer 15 constitute an MIM element serving as a two-terminal active element consisting of metal-insulator-metal.

An insulating film 16 consisting of various insulators is formed on the wiring layer 12, the first electrode portion 13, the thin insulating layer 14, and the second electrode layer 15. The insulating film 16 entirely covers the wiring layer 12 and the surface of the MIM element as shown in FIG. 1. Part of the insulating film 16 is slightly inserted into the inner side of a pixel region.

Here, the insulating film 16 is formed on the second electrode layer 15 except for the surface of the pixel contact portion 15b, a portion corresponding to the pixel contact portion 15b is used as an opening for assuring the electric conductivity between the MIM element and a pixel electrode 17 (to be described later).

In each pixel region in which one MIM element described above is formed, the transparent pixel electrode 17 consisting of an ITO is formed. The peripheral portion of the pixel electrode 17 is formed to overlap the inner edge portion of the insulating film 16. The peripheral portion is designed to be in contact with the pixel contact portion 15b of the second electrode layer 15 through the opening.

As shown in FIG. 2, a color filter 21 constituted by

coloring layers 21C each of which exhibits one color of red (R), green (G), and blue (B) and a black matrix layer 21BM formed between the coloring layers 21C is formed on the inner surface of an opposite substrate 20. A protective film 22 consisting of a transparent resin is adhered to the surface of the color filter 21. The protective film 22 is used to protect the color filter 21. An opposite electrode 23 consisting of an ITO formed in the form of a stripe is formed on the surface of the protective film 22. Here, the coloring layers in this embodiment are not limited to the three coloring layers which exhibit red, green, and blue, and coloring layers which exhibit cyan, magenta, and yellow may be used.

In addition, orientation films are formed on the element substrate 10 and the inner surface of the opposite substrate 20 by painting, and subjected to a rubbing process. A liquid-crystal layer 30 is arranged to be held between these orientation films.

In this embodiment, the insulating film 16 completely covers the wiring layer 12 and the surface of the MIM element. Even if conductive foreign matter is inserted between the element substrate 10 and the opposite substrate 20, there is no risk that the wiring layer 12, the MIM element, and the opposite electrode are in electric contact with each other. Therefore, a crossline-like line defect which is severe for a liquid-crystal display device can be prevented from being generated.

Since the insulating film 16 is interposed between the wiring layer 12 and the pixel electrode 17 except for a position where the pixel contact portion 15b of the second electrode layer 15 is in electric contact with the pixel electrode 17, the short circuit between the wiring layer 12 and the pixel electrode 17 and a leak current therebetween can be prevented from being generated.

FIGS. 3 to 6 show main manufacturing steps of the element substrate 10 in the embodiment. As shown in FIG. 3, Ta is coated on the entire area of the surface of the element substrate 10 consisting of non-alkaline glass by a sputtering method, and is thermally oxidized to form an underlying layer 11 consisting of a Ta oxide. This underlying layer may be formed by RF-sputtering of a Ta oxide. Ta or a Ta alloy is coated on the surface of the underlying layer 11 again by a sputtering method. This coating is patterned by photolithography to form the wiring layer 12 (see FIG. 1, not shown in FIG. 3). Here, the first electrode portion 13 is integrally formed with the wiring layer 12 as described above.

As shown in FIG. 4, the thin insulating layer 14 is formed on the surfaces of the wiring layer 12 and the first electrode portion 13 by an anodic oxidation method. The thin insulating layer 14 is formed in the following manner. That is, the element substrate 10 is dipped in an electrolytic solution, and a current flows between the wiring layer 12 and the opposite electrode in this electrolytic solution to oxidize the surface

of the Ta film. Since this thin insulating layer 14 is to determine the electric characteristics of the MIM element, film forming conditions, a film thickness, and the like must be precisely set. After the thin insulating layer 14 is formed, the thin insulating layer 14 may be subjected to annealing (thermal process) as needed.

In addition, Cr is coated on the thin insulating layer 14 formed on the surface of the first electrode portion 13 by a sputtering method. Predetermined patterning is performed to the Cr film to form the second electrode layer 15. The second electrode layer 15, as shown in FIG. 1, has the electrode portion 15a formed to partially overlap the first electrode portion 13 and the pixel contact portion 15b to be in electric contact with the pixel electrode 17 (to be described later). The second electrode layer 15 may consist of aluminum, titanium, molybdenum, or the like.

As shown in FIG. 5, the insulating film 16 having a predetermined thickness is formed on the surface of the element substrate 10. As this insulating film, an inorganic insulating film formed by a sputtering method or the like and consisting of a Ta oxide, a silicon oxide, a silicon nitride, an aluminum oxide, or the like is preferably used. The insulating film 16 is formed to have a film thickness which can assure insulating properties between the insulating film 16 and the wiring layer 12 or the like formed as an underlying layer. For example, a Ta oxide film preferably has about 1,000Å or more, and a silicon

oxide film preferably has about 400Å or more. In general, the thickness of the insulating film 16 is set to about 400 to 5,000Å. The insulating film 16 may consist of a polyimide resin, an acrylic resin, or the like.

The insulating film 16 formed as described above is patterned by a photolithographic process using a mask having a planar pattern which is almost the same as that of the transparent region (portion except for the region in which the black matrix layer 21BM is formed) of the color filter 21 on the opposite substrate 20 shown in FIG. 2, and a region A corresponding to the pixel region of the liquid-crystal display device in FIG. 5 and a region B serving as an opening corresponding to the pixel contact portion 15b of the second electrode layer 15 are removed.

Here, when the mask for forming the transparent region of the color filter is also used in the patterning of the insulating film 16, the number of masks used in the photolithographic process can be reduced, and manufacturing cost can be reduced.

Finally, as shown in FIG. 6, an ITO is coated on the entire area of the resultant structure by a sputtering method and then patterned to form the pixel electrode 17. This pixel electrode 17 completely covers the region A from which the insulating film 16 is removed, and is adhered to the pixel contact portion 15b of the second electrode layer 15 in the region B. The pixel electrode 17 is formed such that the peripheral portion of the

pixel electrode 17 is located on the inner edge portion of the portion (corresponding to the region A) from which the insulating film 16 is removed.

According to the manufacturing method, since the surfaces of the wiring layer 12 and the MIM element have been completely covered with the insulating film 16 in the formation of the pixel electrode 17, the pixel electrode 17 can be formed without risk that the pixel electrode 17 and the wiring layer 12 or the MIM element are short-circuited. As a result, the pixel electrode 17 can also be maximally formed in the region for forming the wiring layer 12, and an opening ratio can be increased.

Here, after the first electrode portion 13 and the insulating layer 14 are formed, the insulating film 16 is formed to expose the insulating layer 14, and the pixel electrode 17 may be formed on the insulating film 16 by, e.g., an ITO, to be connected to the insulating layer 14. In this case, since the second electrode layer and the pixel electrode are integrally formed with each other, patterning of the second electrode layer can be made unnecessary, and the manufacturing steps of the two-terminal active element can be partially omitted.

The insulating film 16 may be constituted by an insulating film having light-shielding properties. In this case, the black matrix layer 21BM need not be formed on the opposite substrate 20 shown in FIG. 2. For this reason, adhering between

the element substrate and the opposite substrate does not require a high alignment precision. In addition, the structure of the opposite substrate can be simplified, and manufacturing cost can be reduced.

The element substrate 10 and the opposite substrate 20 formed as described above are adhered to each other through a seal member 41, as shown in FIG. 7. A liquid crystal is injected into a liquid-crystal sealing region divided by the seal member to form the liquid-crystal layer 30, and peripheral circuit parts of drive circuits 42a and 42b are packaged, thereby constituting a liquid-crystal display device.

In the embodiment described above, the insulating film 16 is formed on an element substrate having MIM elements. However, according to the present invention, when a wiring layer and a liquid-crystal application electrode are formed on the inner surface of any one of an element substrate and an opposite substrate, the same effect as described above can be obtained by forming an insulating film on the wiring layer of the corresponding substrate.

(Second Embodiment)

An embodiment in which the present invention is applied to a matrix liquid-crystal display device comprising a TFT (Thin-Film Transistor) element as a three-terminal active element will be described below.

FIG. 8 is a plan view showing the brief structure of a pixel of a liquid-crystal display device, comprising a TFT

(Thin-Film Transistor) element, as the liquid-crystal display device according to the present invention, and FIG. 9 is a sectional view showing the pixel along an A - A line in FIG. 8.

A pixel portion will be described first. In FIG. 8, reference numeral 51a denotes a first polysilicon layer constituting an active layer (source region 50a · channel region 50b · drain region 50c) of the TFT. As shown in FIG. 9, a gate insulating film 62 is formed by thermal oxidation. Reference numeral 52a denotes a scanning line serving as a gate electrode of the TFT, and reference numeral 53a denotes a data line, formed to cross the scanning line 52a, for applying a voltage to be applied to a pixel electrode to the source region 50a of the TFT. The scanning line 52a and the data line 53a are formed by the second polysilicon layer and a conductive layer such as an aluminum layer, respectively.

Reference numeral 54 denotes a contact hole for connecting the pixel electrode 56a consisting of an ITO to the drain region 50c of the TFT of the first polysilicon layer 51a, and reference numeral 55 denotes a contact hole for connecting the data line 53a to the source region of the TFT of the first polysilicon layer 51a.

In FIG. 9 showing the section along the A - A line in FIG. 8, reference numeral 10 denotes a substrate such as a glass substrate, a quartz substrate, or a silicon substrate, and reference numeral 62 denotes a gate insulating film such as

a silicon oxide film or the like formed on the surface of the first polysilicon layer 51a serving as the active layer of the TFT. The gate insulating film 62 is formed by thermal oxidation or the like. Reference numeral 63 denotes a first interlayer insulator constituted by an NSG film (silicate glass film being free from boron or phosphorus) or the like. The first interlayer insulator 63 is formed by a high-temperature CVD method and a low-temperature CVD method.

In this embodiment, an insulating layer 64 is formed to cover the upper portion of the channel region 50b, the scanning line 52a serving as the wiring layer and the data line 53a.

The insulating layer 64 is formed on the scanning line 52a and the data line 53a except for the peripheral portion of the contact hole for performing connection to the pixel electrode 56a (to be described later), and the insulating layer 64 is patterned such that the inner peripheral portion of the contact hole 54 partially overlaps the outer peripheral portion of the pixel electrode 56a.

In this embodiment, as the insulating layer 64, an inorganic insulating film formed by a sputtering method or the like and consisting of a Ta oxide, a silicon oxide, a silicon nitride, an aluminum oxide, or the like is preferably used.

The insulating layer 64 is preferably used to have a film thickness which can sufficiently assure the insulating properties between the insulating layer 64 and the data line 53a. For example, a Ta oxide film preferably has about 1,000Å

or more, and a silicon oxide film preferably has about 400Å or more.

The insulating layer 64 can prevent the pixel electrode 56a and the data line 53a from being short-circuited even if they are sufficiently close to each other, so that a defect in display can be prevented from being generated.

When a mask for forming the transparent region of a color filter (not shown) arranged to be opposite to the substrate 10 is used in the patterning of the insulating film 64, the number of masks used in the photolithographic process used when the liquid-crystal display device is manufactured can be reduced.

In this embodiment, when the insulating film 64 consists of a light-shielding material, a leak current can be prevented from being generated by light irradiated on the wiring layer of the TFT element or the region of the active layer, and a black matrix is formed on the element substrate. For this reason, a black matrix need not be formed on the opposite substrate, and adhering between the element substrate and the opposite substrate does not require a high alignment precision.

In addition, the structure of the opposite substrate can be simplified, and manufacturing cost can be reduced.

Thereafter, the contact hole 54 passing through the first interlayer insulator 63 and the gate insulating film 62 is formed, so that the pixel electrode 56a consisting of an ITO is formed.

This embodiment describes the case wherein the ITO is used as the pixel electrode. However, the present invention is not limited to the embodiment, and a transparent electrode material consisting of a high refractory metal oxide such as SnO or ZnOx can also be used.

The element-side substrate having the above structure and an opposite electrode 23 arranged to be opposite to the element-side substrate are adhered to each other through a seal member 41, as shown in FIG. 10. Reference numeral 25 denotes a light-shielding layer formed on the opposite substrate 20 and constituted by a chromium layer or the like.

A liquid crystal 30 is injected into a liquid-crystal sealing region divided by the seal member 41, and a circuit substrate (not shown) for inputting a video signal or the like displayed in the pixel region is packaged on an input terminal portion 90, so that the liquid-crystal display device is constituted.

In this embodiment, transistors constituting peripheral circuits 70 and 80 for supplying drive signals to the TFT elements are constituted by so-called polysilicon TFTs which uses polysilicon layers as operation layers like the TFT elements for pixel driving. These transistors constituting the peripheral circuits are simultaneously formed on the substrate together with the TFT elements for pixel driving by the same process.

In this embodiment, since the insulating layer 64 is formed

to cover the scanning line serving as a wiring layer, even if conductive foreign matter is inserted between the scanning line and the opposite electrode, the scanning line and the opposite electrode are not short-circuited. As a result, a defect caused by the conductive foreign matter can be prevented from being generated.

Although a liquid-crystal display device is described as a transparent liquid-crystal display device in each of the embodiments of the present invention, the present invention is not limited to these embodiments. The present invention may be applied to a reflective liquid-crystal display device in which a reflective layer is formed on one of an element-side substrate and an opposite substrate.

Industrial Applicability

The present invention described above can provide a structure suitable for preventing a display defect caused by a short circuit between upper and lower electrodes by using a structure in which two substrates of a liquid-crystal display device are not easily short-circuited, and a method of manufacturing the same.

CLAIMS

1. A liquid-crystal display device comprising two substrates which interpose a liquid-crystal layer therebetween, a wiring layer formed on at least one inner surface of said substrates, and a pixel electrode directly or indirectly connected to a connection portion of said wiring layer, characterized in that an insulating film is formed on a surface of said wiring layer.

2. A liquid-crystal display device according to claim 1, characterized in that said insulating film is arranged to insulate said wiring layer and said pixel electrode from each other, and has an opening for assuring the electric conductivity between the connection portion and said pixel electrode.

3. A liquid-crystal display device according to claim 1, characterized in that an MIM element is formed between the connection portion and said pixel electrode, and said insulating film also covers the surface of said MIM element.

4. A liquid-crystal display device according to claim 1, characterized in that a TFT element is formed between the connection portion and said pixel electrode, and said insulating film is formed on said wiring layer connected to said TFT element.

5. A liquid-crystal display device according to claim 1, characterized in that said insulating film has light-shielding properties.

6. A method of manufacturing a liquid-crystal display

device comprising two substrates which interpose a liquid-crystal layer therebetween, a wiring layer formed on at least one inner surface of said substrates, and a pixel electrode directly or indirectly connected to a connection portion of said wiring layer,

characterized in that, after said wiring layer is formed on the inner surface of said substrates, an insulating film is formed to cover the surface of said wiring layer and a portion between said wiring layer and said pixel electrode, and then, said pixel electrode is formed such that the peripheral portion of said pixel electrode is arranged on said insulating film.

7. A method of manufacturing a liquid-crystal display device, according to claim 5, characterized in that, after an MIM element connected between the connection portion and said pixel electrode is formed, said insulating film is formed to also cover the surface of said MIM element.

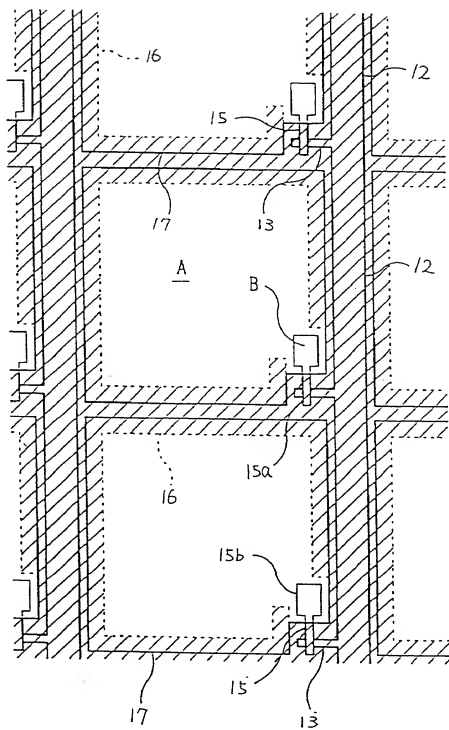
ABSTRACT

A structure in which two substrates in a liquid-crystal display device are not easily short-circuited is used to prevent a display defect caused by a short circuit formed between upper and lower electrodes by insertion of conductive foreign matter.

An insulating film 16 consisting of various insulators is formed on a wiring layer 12, a first electrode portion 13, a thin insulating layer 14, and a second electrode layer 15.

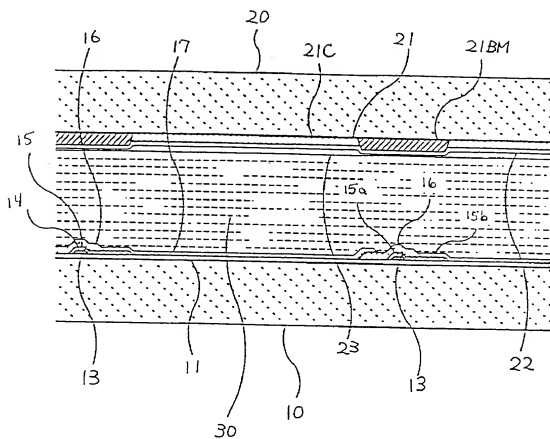
The insulating film 16 entirely covers the surfaces of the wiring layer 12 and the MIM element, and part of the insulating film 16 is slightly inserted into the inner side of a pixel region. A portion of the insulating film 16 corresponding to a pixel contact portion 15b serves as an opening for assuring the electric conductivity between the MIM element and a pixel electrode 17 (to be described later). The peripheral portion of the pixel electrode 17 is formed to overlap the inner edge portion of the insulating film 16. The peripheral portion is designed to be in contact with the pixel contact portion 15b of the second electrode layer 15 through the opening of the insulating film 16.

[FIG. 1]

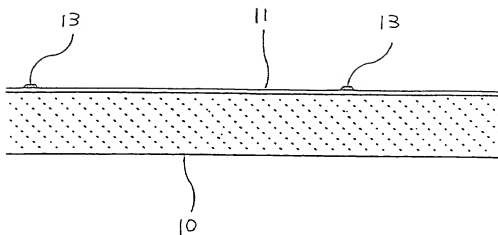


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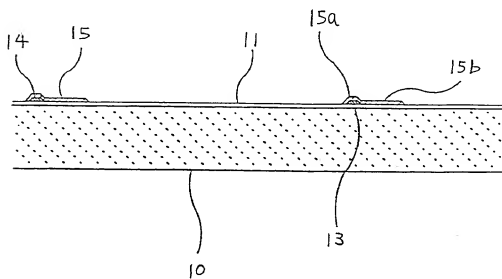
[FIG. 2]



[FIG. 3]

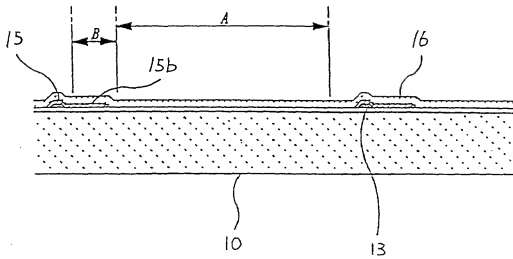


[FIG. 4]

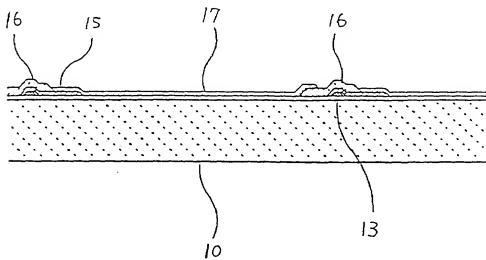


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[FIG. 5]

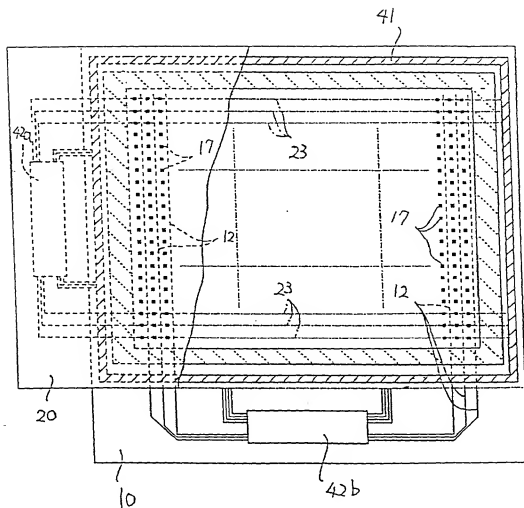


[FIG. 6]



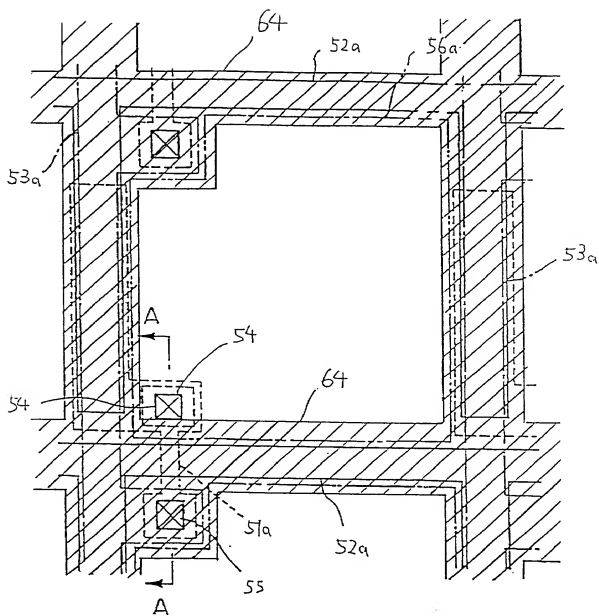
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[FIG. 7]



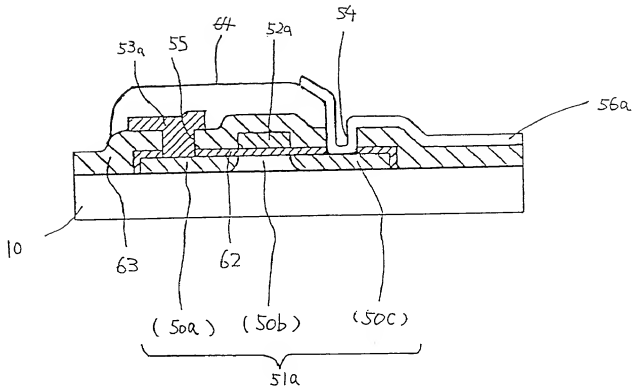
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[FIG. 8]

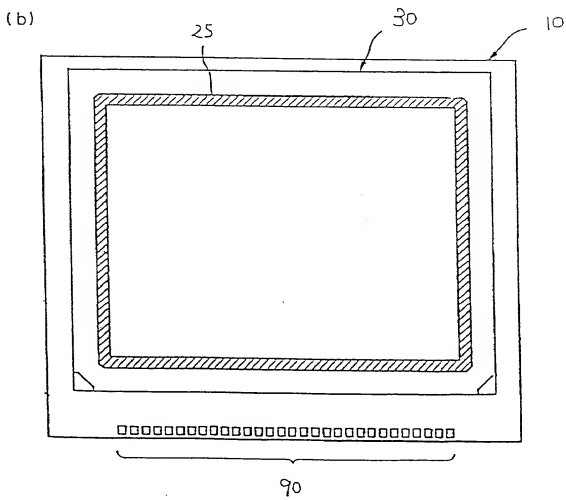
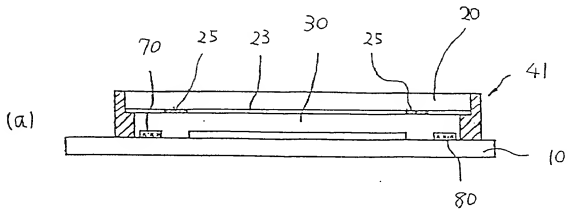


[FIG. 9]

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[FIG. 10]



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9319S-000063

Declaration and Power of Attorney For Patent Application

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I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

液晶表示装置及びその製造方法Liquid-Crystal Display Device andMethod of Manufacturing the Same

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Prior Foreign Application(s)

外国での先行出願
8-229071

(Number)
(番号)

Japan

(Country)
(国名)

29/8/96

(Day/Month/Year Filed)
(出願年月日)

Priority Not Claimed
優先権主張なし

(Number)
(番号)

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(Day/Month/Year Filed)
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PCT/JP97/03018

28/8/97

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Pending

(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

(Application No.)
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G. Gregory Schivley, Reg. No. 27,382,

POWER OF ATTORNEY: As a named inventor, I hereby appoint
the following attorney(s) and/or agent(s) to prosecute this
application and transact all business in the Patent and Trademark
Office connected therewith (first name and registration number)

Bryant E. Wade, Reg. No. 40,344

書類送付先

Send Correspondence to:
G. Gregory Schivley, Esq.
Harness, Dickey & Pierce, P.L.C.
P.O. Box 828
Bloomfield Hills, MI 48303

直接電話連絡先: (名前及び電話番号)

Direct Telephone Calls to: (name and telephone number)

G. Gregory Schivley
(248) 641-1600

唯一または第一発明者名

小出 清貴

Full name of sole or first inventor

Kiyotaka KOIDE

発明者の署名

小出 清貴

日付

1998.4.29

Inventor's signature

Kiyotaka Koide

Date

1998.4.29

住所

日本国長野

Residence

Nagano, Japan (JX)

国籍

日本国

Citizenship

Japan

私書箱

392-8502 日本国長野県諏訪市大和 c/o

Post Office Address

Seiko Epson Corp., 3-5 Owa 3-chome

3丁目3-5 セイコエプソン株式会社

Suwa-shi, Nagano-ken 392-8502 JAPAN

第二共同発明者

Full name of second joint inventor, if any

第二共同発明者

日付

Second inventor's signature

Date

住所

Residence

国籍

Citizenship

私書箱

Post Office Address

(第三以降の共同発明者についても同様に記載し、署名を
すること)

(Supply similar information and signature for third and subsequent
joint inventors.)